

Version of Amended Claims Showing Changes

14. (Amended) A circuit board comprising:

a substrate having an array of vias, the array of vias defining a set of channels on the substrate; and

three bus traces routed on the circuit board so as to enter a first channel belonging to the set of channels and to exit a second channel belonging to the set of channels, the first and second channels adjacent to each other so that there is one row of vias between the first and second channels, wherein each of the three bus traces are connected to a via belonging to the row of vias so as to define three connections[, wherein the three connections do not overlap the three bus traces].